

ABSTRACT OF THE DISCLOSURE

A method and a device for bridging temporary power outages in a matrix converter are disclosed. In the event of a detected power failure, the matrix converter is disconnected from the power line and changes into a buffer mode in which a determined actual capacitor voltage space vector is regulated to a pre-determined space vector. When the power line is reestablished, the actual capacitor space vector is synchronized and the matrix converter is reconnected to the power line during the synchronization. This allows a conventional matrix converter to take advantage of kinetic load buffering.